

### **AMENDMENTS TO THE ABSTRACT**

Please cancel the Abstract section of the specification and replace with the following:

A method for generating a Cyclic Redundancy Check (CRC) in a system including steps of creating a circuit comprising a plurality of registers wherein each of the plurality of registers is associated with a corresponding logic gate, and programming a subset of the plurality of registers to have a value of zero and programming a corresponding subset of the logic gates to have a value of zero. The step of programming is based on a pre-selected polynomial key word.